

EENG 426/CPSC 459/ENAS 876 Silicon Compilation

Syntax-directed translation

Computer Systems Lab
<http://csl.yale.edu/~rajit>

Fall 2018

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

1 / 7

Guards

Guards that are built out of local variables:

- Variable interface:
 - A channel to *start the operation*
 - A dual-rail (dualrail) value that holds the value of the variable
- For reads:
 - A dual-rail data channel (a1of2) to send the value out
- For writes:
 - A dual-rail data channel to receive the value

Yale

AVLSI

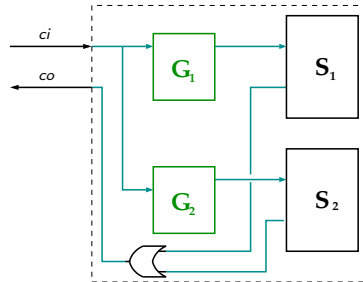
Manohar

EENG 426: Silicon Compilation

Fall 2018

2 / 7

Syntax-directed translation



(In this version, only local variables are used)

Yale

AVLSI

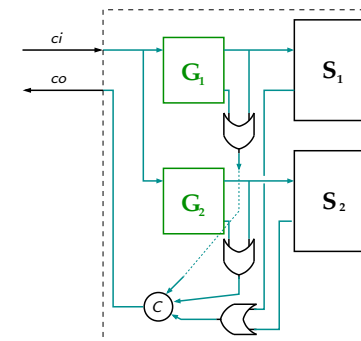
Manohar

EENG 426: Silicon Compilation

Fall 2018

3 / 7

Alternative compilation of selection



Yale

AVLSI

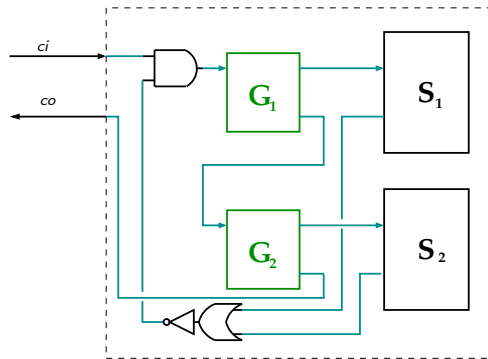
Manohar

EENG 426: Silicon Compilation

Fall 2018

4 / 7

Loops



Yale

AVLSI

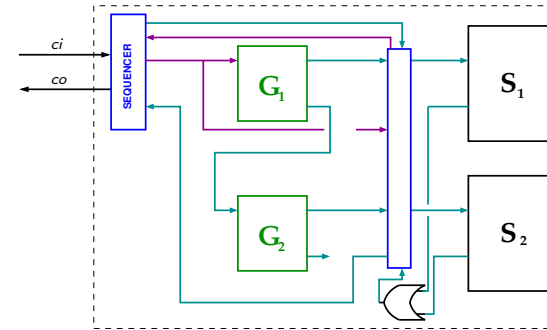
Manohar

EENG 426: Silicon Compilation

Fall 2018

5 / 7

What if S_i changes G_i ?



Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

6 / 7

Probed guards

How do we handle probes?

- Probes are the request line for the channel
 - The probed end is *passive*
 - If there is data, OR the two data rails
- Evaluate all the guards in parallel
- Combine them with the probe

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

7 / 7