

EENG 426/CPSC 459/ENAS 876

Silicon Compilation

Handshaking expansions

Computer Systems Lab

<http://cs1.yale.edu/~rajit>

Fall 2018

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

1 / 19

Handshaking expansions

Handshaking expansions are CHP programs, with the following restrictions:

- only Boolean-valued variables
- no communication actions
- only constants on the RHS of assignments

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

3 / 19

Handshaking expansions

CHP: high level constructs such as send and receive

* [$L?x; R!x$]

Production rules: circuit description

$$\begin{aligned} a \wedge b &\mapsto c \downarrow \\ \neg a \vee \neg b &\mapsto c \uparrow \end{aligned}$$

Handshaking expansions: intermediate form

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

2 / 19

Handshaking expansions

Replace

$x := y$

with

[$y \rightarrow x\uparrow \sqcap \neg y \rightarrow x\downarrow$]

Variables of a process are classified into:

- internal (local variables, not shared)
- input (shared, only read by the process)
- output (shared, written by the process)

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

4 / 19

Handshake protocols

We first consider the case when we have bare communication actions (no data being sent/received).

Synchronization is implemented by using two wires.

Two-phase handshake: (initially all variables are false)

$$\begin{aligned} X &: xo \uparrow; [xi] \\ Y &: [yi]; yo \uparrow \end{aligned}$$

X: active communication protocol

Y: passive communication protocol

$$xo = yi; yo = xi$$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

5 / 19

Handshake protocols

Problem: it is not always possible to alternate the two implementations.

$$\begin{aligned} [B \rightarrow X] \\ [\neg B \rightarrow \text{skip}] \end{aligned}$$

General solution: use the *same* implementation for the two.

(Two-phase handshake protocol)

$$\begin{aligned} X &: xo := \neg xo; [xi = xo] \\ Y &: [yi \neq yo]; yo := \neg yo \end{aligned}$$

This implementation is costly.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

7 / 19

Handshake protocols

If all variables are true initially:

$$\begin{aligned} X &: xo \downarrow; [\neg xi] \\ Y &: [\neg yi]; yo \downarrow \end{aligned}$$

Both protocols synchronize the two actions.

Since the final state of one implementation is the initial state of the other, we can *alternate* the two implementations.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

6 / 19

Handshake protocols

Four-phase handshaking:

$$\begin{aligned} X &: xo \uparrow; [xi]; xo \downarrow; [\neg xi] \\ Y &: [yi]; yo \uparrow; [\neg yi]; yo \downarrow \end{aligned}$$

X: active communication protocol
Y: passive communication protocol

The waits are simplified, which results in better circuits.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

8 / 19

Handshaking expansions

Example:

* $[L; R]$

becomes:

* $[i]; lo\uparrow; \neg i; lo\downarrow; ro\uparrow; ri; ro\downarrow; \neg ri]$

or:

* $[lo\uparrow; i; lo\downarrow; \neg i; ri; ro\uparrow; \neg ri; ro\downarrow]$

or:

* $[i; lo\uparrow; ro\uparrow; ri; \neg i; lo\downarrow; ro\downarrow; \neg ri]$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

9 / 19

AVLSI



Fall 2018 10 / 19

Implementing probes

Example:

* $[\bar{L} \rightarrow R; L]$

becomes:

* $[i \rightarrow ro\uparrow; ri; ro\downarrow; \neg ri; lo\uparrow; \neg i; lo\downarrow]$

If port X is probed, we implement it using a **passive** communication protocol.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

11 / 19

AVLSI



Fall 2018 12 / 19

Implementing probes

Example:

* $[\bar{C} \rightarrow x\uparrow; C \ \bar{D} \rightarrow x\downarrow; D]$

becomes:

* $[[ci \rightarrow x\uparrow; \underbrace{[ci]}_{omit}; co\uparrow; \neg ci; co\downarrow;$
 $\bar{di} \rightarrow x\downarrow; \underbrace{[di]}_{omit}; do\uparrow; \neg di; do\downarrow]]$

or:

* $[[ci \rightarrow x\uparrow; co\uparrow; \neg ci; co\downarrow;$
 $\bar{di} \rightarrow x\downarrow; do\uparrow; \neg di; do\downarrow]]$

Yale

AVLSI

Manohar EENG 426: Silicon Compilation

Fall 2018 10 / 19

Reshuffling

Since the first part of the four-phase protocol synchronizes the two actions, we can postpone the last part of the protocol.

- This transformation is called **reshuffling**
- Reshuffling changes the order in which signals change
⇒ different circuit!
- Circuit efficiency can be significantly altered

Reshuffle with care: you might introduce deadlock!

Yale

AVLSI

Manohar EENG 426: Silicon Compilation

Fall 2018 12 / 19

Lazy-active handshake protocol

Lazy-active protocol:

$$X : [\neg xi]; xo\uparrow; [xi]; xo\downarrow$$

The wait for xi to be **false** can always be postponed until the next time the handshake protocol is executed.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

13 / 19

Analysis of reshuffling

Passive, passive:

$$\begin{array}{lcl} [li]; lo\uparrow; ri; ro\uparrow & \triangleright & L^+; R^+ \\ [li \wedge ri]; lo\uparrow, ro\uparrow & \triangleright & L^+ \star R^+ \\ ri; ro\uparrow; li; lo\uparrow & \triangleright & R^+; L^+ \end{array}$$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

15 / 19

Analysis of reshuffling

We can analyze the effect of a reshuffling at the CHP level of abstraction!

A 4-phase handshake is two 2-phase handshakes—i.e., two synchronizations.

- Write all handshaking using 2-phase CHP: only two-phase handshakes allowed. $L_{4\phi} \triangleright L_{2\phi}^+; L_{2\phi}^-$
- Relation between two 2-phase handshakes L and R ?
 - Test against two environments: $L; R$ and $R; L$!
 - If both work, then parallel
 - If only one works, in sequences
 - If neither work, then “ \star ”

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

14 / 19

Analysis of reshuffling

Active, active:

$$\begin{array}{lcl} lo\uparrow; li; ro\uparrow; ri & \triangleright & L^+; R^+ \\ lo\uparrow, ro\uparrow; li \wedge ri & \triangleright & L^+ \parallel R^+ \\ ro\uparrow; ri; lo\uparrow; li & \triangleright & R^+; L^+ \end{array}$$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

16 / 19

Analysis of reshuffling

Active, passive:

$$lo\uparrow; [li \wedge ri]; ro\uparrow \triangleright L^+; R^+$$

$$lo\uparrow; [ri]; ro\uparrow; [li] \triangleright L^+ \| R^+$$

$$[ri]; lo\uparrow; [li]; ro\uparrow \triangleright L^+ \star R^+$$

$$[ri]; ro\uparrow, lo\uparrow; [li] \triangleright R^+; L^+$$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

17 / 19

Implementing bullets

$$L \bullet R \triangleright L^+; R^+; L^-; R^-$$
$$\triangleright L^+; R^+; R^-; L^-$$

Interleave the parts of the handshaking expansion so that neither L nor R can complete unless the other has begun.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

19 / 19

Reshuffling examples

$$*[li]; ro\uparrow; ri; ro\downarrow; \neg ri; lo\uparrow; \neg li; lo\downarrow]$$

$$\triangleright *[L^+ \star (R^+; R^-); L^-]$$

$$*[li]; ro\uparrow; ri; lo\uparrow; \neg li; ro\downarrow; \neg ri; lo\downarrow]$$

$$\triangleright *[L^+ \star R^+; L^- \star R^-]$$

$$*[li]; lo\uparrow; \neg li; lo\downarrow; ro\uparrow; ri; ro\downarrow; \neg ri]$$

$$\triangleright *[L^+; L^-; R^+; R^-]$$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

18 / 19