

# EENG 426/CPSC 459/ENAS 876

## Silicon Compilation

### Non-deterministic selections

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# Arbitration

Consider the following CHP program:

```
*[ $\overline{X} \rightarrow X?_x$   
  |  $\overline{Y} \rightarrow Y?_x$   
  ];  
  Z!_x  
]
```

When  $\overline{X}$  and  $\overline{Y}$  are both **true**, we have to pick one of them and execute the appropriate branch of the selection statement.

**Arbitration** is the mechanism that picks one of two alternatives, deciding which alternative came "first."

An **arbiter** is the following process:

$$\text{Arb}(a, b, u, v) \equiv \quad * [[a \longrightarrow u\uparrow; [\neg a]; u\downarrow \\ \quad \quad \quad | b \longrightarrow v\uparrow; [\neg b]; v\downarrow \\ \quad \quad \quad ]]$$

The process does a handshake on  $(a, u)$  and  $(b, v)$ .  
Suppose we try and write production rules:

$$a \wedge \neg v \mapsto u\uparrow$$

$$\neg a \vee v \mapsto u\downarrow$$

$$b \wedge \neg u \mapsto v\uparrow$$

$$\neg b \vee u \mapsto v\downarrow$$

To make the circuit directly implementable, we flip the sense of variables  $u$  and  $v$ .

$$\begin{aligned}a \wedge \neg v &\mapsto \neg u \downarrow \\ \neg a \vee \neg \neg v &\mapsto \neg u \uparrow\end{aligned}$$

$$\begin{aligned}b \wedge \neg u &\mapsto \neg v \downarrow \\ \neg b \vee \neg \neg u &\mapsto \neg v \uparrow\end{aligned}$$

$\Rightarrow$  cross-coupled NAND gates.

What happens if both  $a$  and  $b$  go up at the same time?

The signals will separate eventually; however, we don't know how long it will take. It is impossible to have a circuit that decides which input switched first in bounded time.

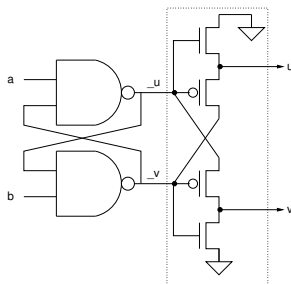
$$\Pr[\text{time} \geq t] = Ae^{-t/\tau_0}$$

Note: the **average time** taken for signals to separate is bounded.

Since our circuits are asynchronous, we can wait until the signals separate.

# Arbiters

The output of the cross-coupled NAND gate is connected to a filter circuit that waits for the signals to be separated by a threshold voltage.



(Note that the CMOS circuit is indeed weakly fair!)

# Arbitration

Simple example:

$$\begin{aligned} &* [ \overline{A} \longrightarrow X; A \\ &\quad | \overline{B} \longrightarrow Y; B \\ &] ] \end{aligned}$$

Handshaking:

$$\begin{aligned} &* [ [ai \longrightarrow xo\uparrow; [xi]; ao\uparrow; [\neg ai]; xo\downarrow; [\neg xi]; ao\downarrow \\ &\quad | bi \longrightarrow yo\uparrow; [yi]; bo\uparrow; [\neg bi]; yo\downarrow; [\neg yi]; bo\downarrow \\ &] ] \end{aligned}$$

# Arbitration

Introduce new variables  $u$  and  $v$ :

```
* [  $ai \rightarrow u\uparrow; [u]; xo\uparrow; [xi]; ao\uparrow;$   
     $[\neg ai]; u\downarrow; [\neg u]; xo\downarrow; [\neg xi]; ao\downarrow$   
  
  |  $bi \rightarrow v\uparrow; [v]; yo\uparrow; [yi]; bo\uparrow;$   
     $[\neg bi]; v\downarrow; [\neg v]; yo\downarrow; [\neg yi]; bo\downarrow$   
  ] ]
```

The idea is to introduce the output of the arbiter into the handshaking expansion. The next step is to decompose the arbiter out of the handshaking expansion.



# Process factorization

Idea: “factor out” an arbiter!

After process factorization:

$$\begin{aligned} &* \left[ \begin{aligned} &[ai \longrightarrow u\uparrow; [\neg ai]; u\downarrow \\ &\mid bi \longrightarrow v\uparrow; [\neg bi]; v\downarrow \end{aligned} \right. \\ &\left. \right] \end{aligned}$$

||

$$\begin{aligned} &* \left[ \begin{aligned} &[u \longrightarrow xo\uparrow; [xi]; ao\uparrow; [\neg u]; xo\downarrow; [\neg xi]; ao\downarrow \\ &\mid v \longrightarrow yo\uparrow; [yi]; bo\uparrow; [\neg v]; yo\downarrow; [\neg yi]; bo\downarrow \end{aligned} \right. \\ &\left. \right] \end{aligned}$$

# Process factorization

Production rules:

$$\begin{aligned}\neg bo \wedge u &\mapsto xo\uparrow \\ xi &\mapsto ao\uparrow \\ (bo \vee) \neg u &\mapsto xo\downarrow \\ \neg xi &\mapsto ao\downarrow\end{aligned}$$

$$\begin{aligned}\neg ao \wedge v &\mapsto yo\uparrow \\ yi &\mapsto bo\uparrow \\ (ao \vee) \neg v &\mapsto yo\downarrow \\ \neg yi &\mapsto bo\downarrow\end{aligned}$$

# Arbitration with multiplexing

CHP Program:

$$\begin{aligned} &*[\overline{A} \longrightarrow S; A \\ &\quad | \overline{B} \longrightarrow S; B \\ &]\end{aligned}$$

Decomposition:

$$\begin{aligned} &*[\overline{A} \longrightarrow P; A \\ &\quad | \overline{B} \longrightarrow Q; B \\ &]\end{aligned}$$
$$\parallel$$
$$\begin{aligned} &*[\overline{P} \longrightarrow S; P \\ &\quad | \overline{Q} \longrightarrow S; Q \\ &]\end{aligned}$$

# Arbitration with multiplexing

Handshaking:

$$\begin{aligned} & * [[pi \longrightarrow so\uparrow; [si]; po\uparrow; [\neg pi]; so\downarrow; [\neg si]; po\downarrow \\ & \quad [] qi \longrightarrow so\uparrow; [si]; qo\uparrow; [\neg qi]; so\downarrow; [\neg si]; qo\downarrow \\ & \quad ]] \end{aligned}$$

Production rules:

$$\begin{aligned} pi \vee qi &\mapsto so\uparrow \\ \neg pi \wedge \neg qi &\mapsto so\downarrow \end{aligned}$$

$$\begin{aligned} si \wedge qi &\mapsto qo\uparrow \\ (\neg qi \wedge) \neg si &\mapsto qo\downarrow \end{aligned}$$

$$\begin{aligned} si \wedge pi &\mapsto po\uparrow \\ (\neg pi \wedge) \neg si &\mapsto po\downarrow \end{aligned}$$

# Negated probes

Consider the following CHP program:

$$\begin{aligned} &*[[\overline{X} \wedge \overline{S} \longrightarrow S!\mathbf{true}, X \\ &\quad | \neg\overline{X} \wedge \overline{S} \longrightarrow S!\mathbf{false} \\ &]] \end{aligned}$$

This program determines the current value of the probe.  
 $S$  determines when the probe is evaluated.

- Why a thin bar?!

# Negated probes

Assuming the channels are passive, we get the following handshaking expansion:

$$\begin{aligned} & * [ [Xi \wedge Si \longrightarrow Sto\uparrow; [\neg Si]; Sto\downarrow; Xo\uparrow; [\neg Xi]; Xo\downarrow \\ & \quad | \neg Xi \wedge Si \longrightarrow Sfo\uparrow; [\neg Si]; Sfo\downarrow \\ & \quad ] ] \end{aligned}$$

Since the CMOS implementation of a two-way arbiter is weakly fair, we can implement this HSE with the following:

$$\begin{aligned} & * [ [Xi \longrightarrow [Si]; Sto\uparrow; [\neg Si]; Sto\downarrow; Xo\uparrow; [\neg Xi]; Xo\downarrow \\ & \quad | Si \longrightarrow Sfo\uparrow; [\neg Si]; Sfo\downarrow \\ & \quad ] ] \end{aligned}$$

# Negated probes

Introduce arbiter variables:

$$\begin{aligned} & * [[Xi \longrightarrow u\uparrow; [u]; [Si]; Sto\uparrow; [\neg Si]; Sto\downarrow; \\ & \quad \quad \quad Xo\uparrow; [\neg Xi]; u\downarrow; [\neg u]; Xo\downarrow \\ & \quad | Si \longrightarrow v\uparrow; [v]; Sfo\uparrow; [\neg Si]; v\downarrow; [\neg v]; Sfo\downarrow \\ & \quad ]] \end{aligned}$$

Apply process factorization:

$$\begin{aligned} & * [[u \longrightarrow [Si]; Sto\uparrow; [\neg Si]; Sto\downarrow; Xo\uparrow; [\neg u]; Xo\downarrow \\ & \quad \quad \quad \llbracket v \longrightarrow Sfo\uparrow; [\neg v]; Sfo\downarrow \\ & \quad \quad \quad \rrbracket \\ & \quad \quad \quad ]] \\ & \parallel \\ & Arb(Xi, Si, u, v) \end{aligned}$$

# Negated probes

Reshuffled HSE:

$$\begin{aligned} & * [[u \longrightarrow [Ei]; Eto\uparrow; [\neg Ei]; Xo\uparrow; Eto\downarrow; [\neg u]; Xo\downarrow \\ & \quad \llbracket v \longrightarrow Efo\uparrow; [\neg v]; Efo\downarrow \\ & \quad \rrbracket] \end{aligned}$$

Production rules:

$$\begin{aligned} u \wedge \neg Xo \wedge Ei &\mapsto \neg Eto\downarrow \\ \neg \neg Xo &\mapsto \neg Eto\uparrow \end{aligned}$$

$$\begin{aligned} Xo &\mapsto \neg Xo\downarrow \\ \neg Xo &\mapsto \neg Xo\uparrow \end{aligned}$$

$$\begin{aligned} \neg Xo \wedge v &\mapsto Efo\downarrow \\ \neg v &\mapsto Efo\uparrow \end{aligned}$$

$$\begin{aligned} u &\mapsto \neg u\downarrow \\ \neg u &\mapsto \neg u\uparrow \end{aligned}$$

$$\begin{aligned} \neg \neg u \wedge \neg \neg Eto \wedge \neg Ei &\mapsto Xo\uparrow \\ \neg u \wedge \neg Eto &\mapsto Xo\downarrow \end{aligned}$$