

# EENG 426/CPSC 459/ENAS 876 Silicon Compilation

## CHP Examples

Computer Systems Lab  
<http://csl.yale.edu/~rajit>

Fall 2018

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

1 / 7

## Copy and alternator

Copy:

$*[ X?a; Y!a, Z!a ]$

Alternator:

$*[ X?a; Y!a; X?a; Z!a ]$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

2 / 7

## Controlled split and merge

Split:

$*[ C?c; X?a;$   
 $[c \rightarrow Y!a \quad \neg c \rightarrow Z!a ]$   
 $]$

Merge:

$*[ C?c; [c \rightarrow X?a \quad \neg c \rightarrow Y?a];$   
 $Z!a$   
 $]$

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

3 / 7

## Reactive process structure

$*[[G_1 \rightarrow S_1$   
 $\quad [G_2 \rightarrow S_2$   
 $\quad \dots$   
 $\quad [G_n \rightarrow S_n$   
 $]]$

This process structure is used very often.

- Wait for some action to be enabled
- Execute that action
- Repeat

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

4 / 7

## Construction a lazy stack

The problem: construct a last-in first-out structure with capacity  $N$ .

Environment:

- insert:  $push!x$
- remove:  $pop?x$
- operations are mutually exclusive

Program is allowed to fail when attempting to insert into a full stack, or remove from an empty stack.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

5 / 7

## Sequential program

Using an array to store the values in the stack, we can solve the problem as follows:

```
n := 0;
* [[  $\overline{push} \wedge n < N \rightarrow push?x[n]; n := n + 1$ 
    [  $\overline{pop} \wedge n > 0 \rightarrow pop!x[n - 1]; n := n - 1$ 
    ] ]
```

Invariant:

$x[0..n - 1]$  are the elements stored in the stack.

$n > 0 \Rightarrow x[n - 1]$  is the last element that was inserted.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

6 / 7

## Recursive construction

To add concurrency, we would like to construct the stack as the parallel composition of a number of processes.

We construct an  $N$ -place stack by assuming the existence of a  $(N - 1)$ -place stack.

Stack element:

- $push, pop$ : environment interface
- $put, get$ : interface to the smaller stack

(This type of construction is used quite often.)

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

7 / 7

## Base case

$N = 1$ :

```
* [[  $\overline{push} \rightarrow push?x$ 
    [  $\overline{pop} \rightarrow pop!x$ 
    ] ]
```

The “rest of the stack” has no storage.

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

8 / 7

## Base case

Another possibility:

```
{stack is empty}
* [ {stack is empty}
    push?x;
    {stack is full}
    pop!x
  ]
```

What happens when the stack overflows? Underflows?

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

9 / 7

## Stack element

1. Assume the stack element is empty:

```
[push → push?x {full}
 []pop → get?x; pop!x {empty}
 ]
```

2. Assume the stack element is full:

```
[push → put!x; push?x {full}
 []pop → pop!x {empty}
 ]
```

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

10 / 7

## Stack element

$b \equiv$  the stack element is empty

```
b↑;
* [ [b ∧ push → push?x; b↓
    []b ∧ pop → get?x; pop!x
    []¬b ∧ push → put!x; push?x
    []¬b ∧ pop → pop!x; b↑
  ] ]
```

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

11 / 7

## Stack element

Another implementation that is equivalent:

```
* [ {empty}
    [push → push?x
    []pop → get?x
    ];
    {full}
    [push → put!x
    []pop → pop!x
    ]
  ]
```

Yale

AVLSI

Manohar

EENG 426: Silicon Compilation

Fall 2018

12 / 7